TFT DISPLAY SPECIFICATION



WINSTAR Display Co.,Ltd.

華凌光電股份有限公司

SPECIFICATION

MODULE NO.: WF70A7SIAHLNN0#

General Specifications

Item	Dimension	Unit
Size	7.0	inch
Dot Matrix	1024 x RGB x 600(TFT)	dots
Module dimension	169.9(W) x 103.4(H) x 5.6(D)	mm
Active area	154.2144 x 85.92	mm
Dot pitch	0.1506 x 0.1432	mm
LCD type	TFT, Normally White, Transmissive	
View Direction	12 o'clock	
Gray Scale Inversion Direction	6 o'clock	
Aspect Ratio	16:9	
Control IC	NA	
Driver IC	EK79001HE + EK73215BCGA	
Backlight Type	LED, Normally White	
Touch Panel	Without Touch Panel	
Interface	LVDS	
Surface	Anti-Glare	

*Color tone slight changed by temperature and driving voltage.

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Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	℃
Storage Temperature	TST	-30	—	+80	℃

Electrical Characteristics

1. Typical Operation Conditions

Item	Symbol	Values			Unit
		Min.	Typ.	Max.	
Power voltage	DVDD	3.0	3.3	3.6	V
	AVDD	9.4	9.6	9.8	V
	VGH	17	18	19	V
	VGL	-6.6	-6.0	-5.4	V
Input signal voltage	VCOM	3.1	3.3	3.6	V
Input logic high voltage	VIH	0.7 DVDD	-	DVDD	V
Input logic low voltage	VIL	0	-	0.3 DVDD	V

2. Current Consumption

Item	Symbol	Values			Unit
		Min.	Typ.	Max.	
Current for Driver	IGH	-	0.2	1.0	mA
	IGL	-	0.2	1.0	mA
	IDVDD	-	4.0	10	mA
	IAVDD	-	20	50	mA

3. Backlight Driving Conditions

Item	Symbol	Values			Unit
		Min.	Typ.	Max.	
Voltage for LED backlight	VL	16.8	19.2	21.0	V
Current for LED backlight	IL	--	320	--	mA
LED life time	-	-	50,000	-	Hr

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Interface

1. LCM PIN Definition

FPC Connector is used for the module electronics interface. The recommended model is FH12A-40S-0.5SH manufactured by Hirose.

Pin No.	Symbol	I/O	Function
1	VCOM	P	Common Voltage
2	VDD	P	Digital circuit
3	VDD	P	Digital circuit
4	NC	---	No connection
5	Reset	I	Global reset pin
6	STBYB	I	Standby mode, Normally pulled high STBYB = "1", normal operation
7	GND	P	Ground
8	RXIN0-	I	Negative LVDS differential data input
9	RXIN0+	I	Positive LVDS differential data input
10	GND	P	Ground
11	RXIN1-	I	Negative LVDS differential data input
12	RXIN1+	I	Positive LVDS differential data input
13	GND	P	Ground
14	RXIN2-	I	Negative LVDS differential data input
15	RXIN2+	I	Positive LVDS differential data input
16	GND	P	Ground
17	RXCLKIN-	I	Negative LVDS differential clock input
18	RXCLKIN+	I	Positive LVDS differential clock input
19	GND	P	Ground
20	RXIN3-	I	Negative LVDS differential data input
21	RXIN3+	I	Positive LVDS differential data input
22	GND	P	Ground
23	NC	---	No connection
24	NC	---	No connection
25	GND	P	Ground
26	NC	---	No connection
27	DIMO	O	Backlight CABC controller signal output
28	SELB	I	6bit/8bit mode select
29	AVDD	P	Power for Analog Circuit
30	GND	P	Ground

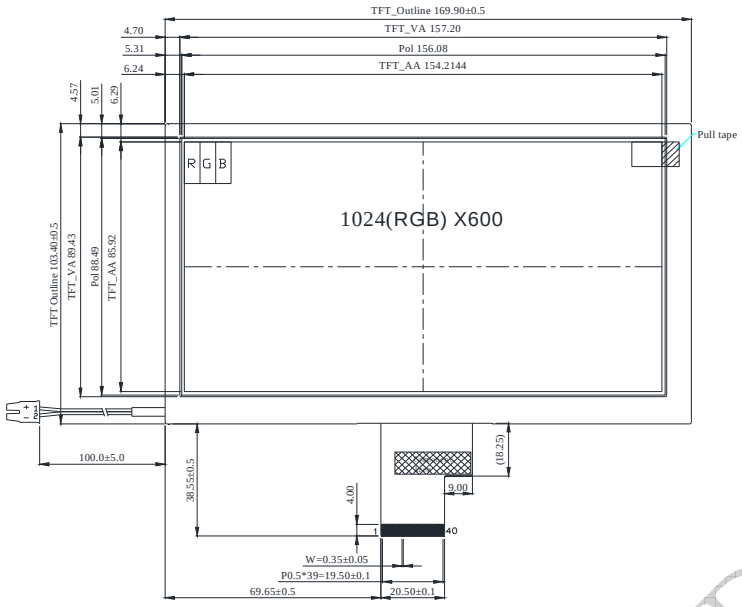
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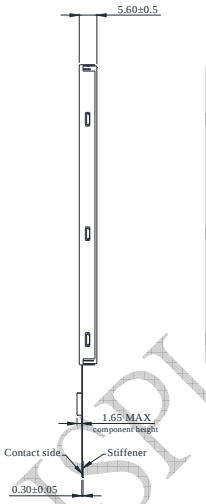
31	NC	---	No connection
32	NC	---	No connection
33	L/R	I	Horizontal inversion
34	U/D	I	Vertical inversion
35	VGL	P	Negative power for TFT
36	GND	P	Ground
37	GND	P	Ground
38	VGH	P	Positive power for TFT
39	NC	---	No connection
40	NC	---	No connection

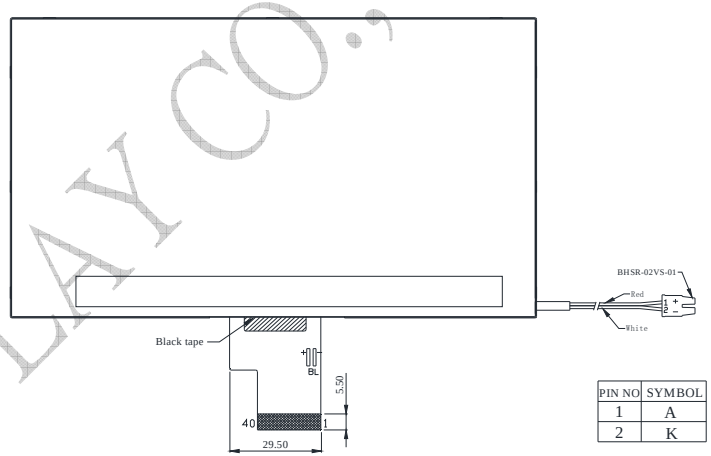
I:input ,O:output,P:power

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Contour Drawing







PIN NO	SYMBOL
1	A
2	K

PIN NO	SYMBOL
1	VCOM
2	VDD
3	VDD
4	NC
5	RESET
6	STBYB
7	GND
8	RXIN0-
9	RXIN0+
10	GND
11	RXIN1-
12	RXIN1+
13	GND
14	RXIN2-
15	RXIN2+
16	GND
17	RXCLKIN-
18	RXCLKIN+
19	GND
20	RXIN3-
21	RXIN3+
22	GND
23	NC
24	NC
25	GND
26	NC
27	DIMO
28	SELB
29	AVDD
30	GND
31	NC
32	NC
33	L/R
34	U/D
35	VGL
36	GND
37	GND
38	VGH
39	NC
40	NC

The non-specified tolerance of dimension is ±0.3mm.

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